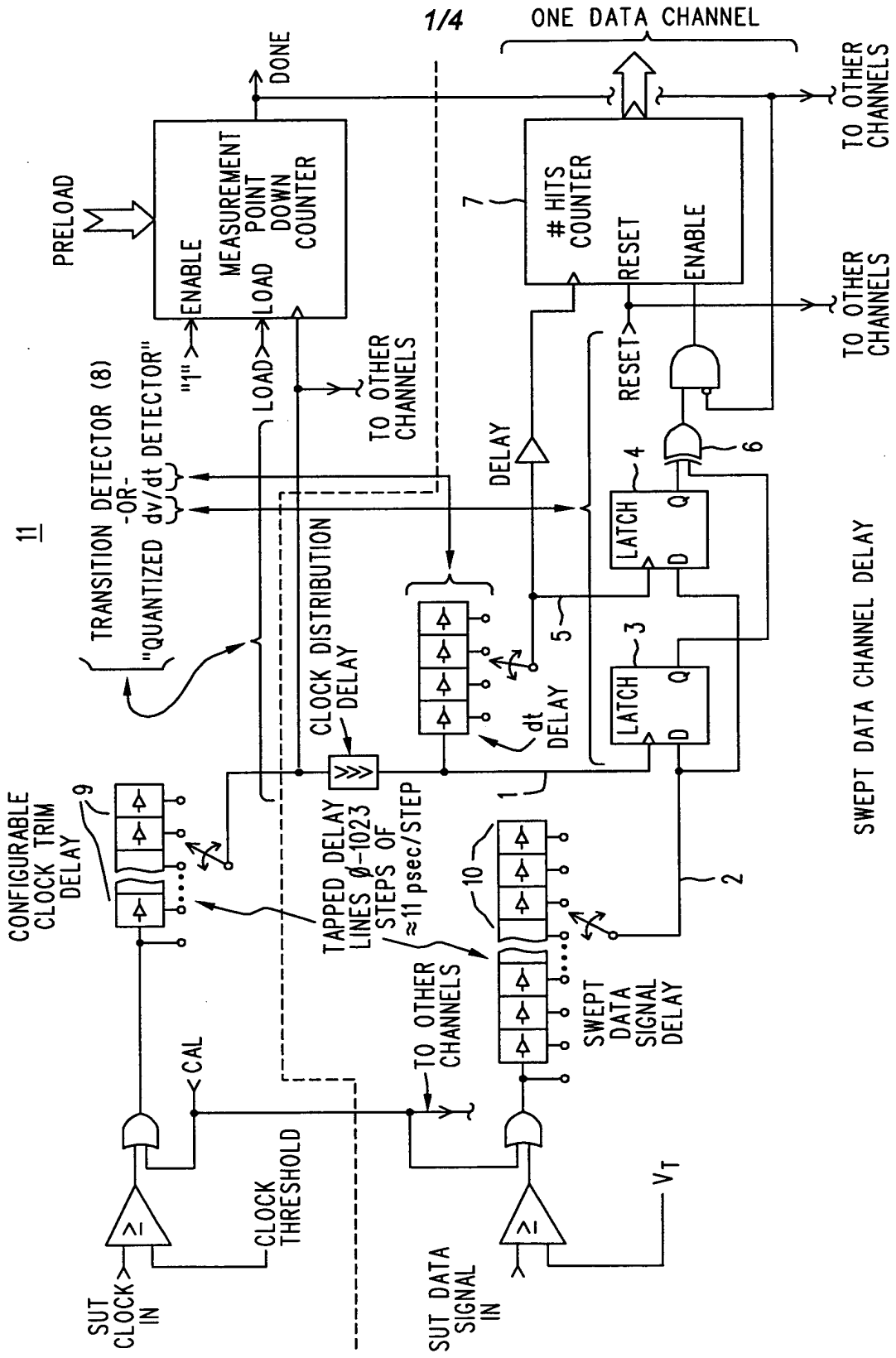
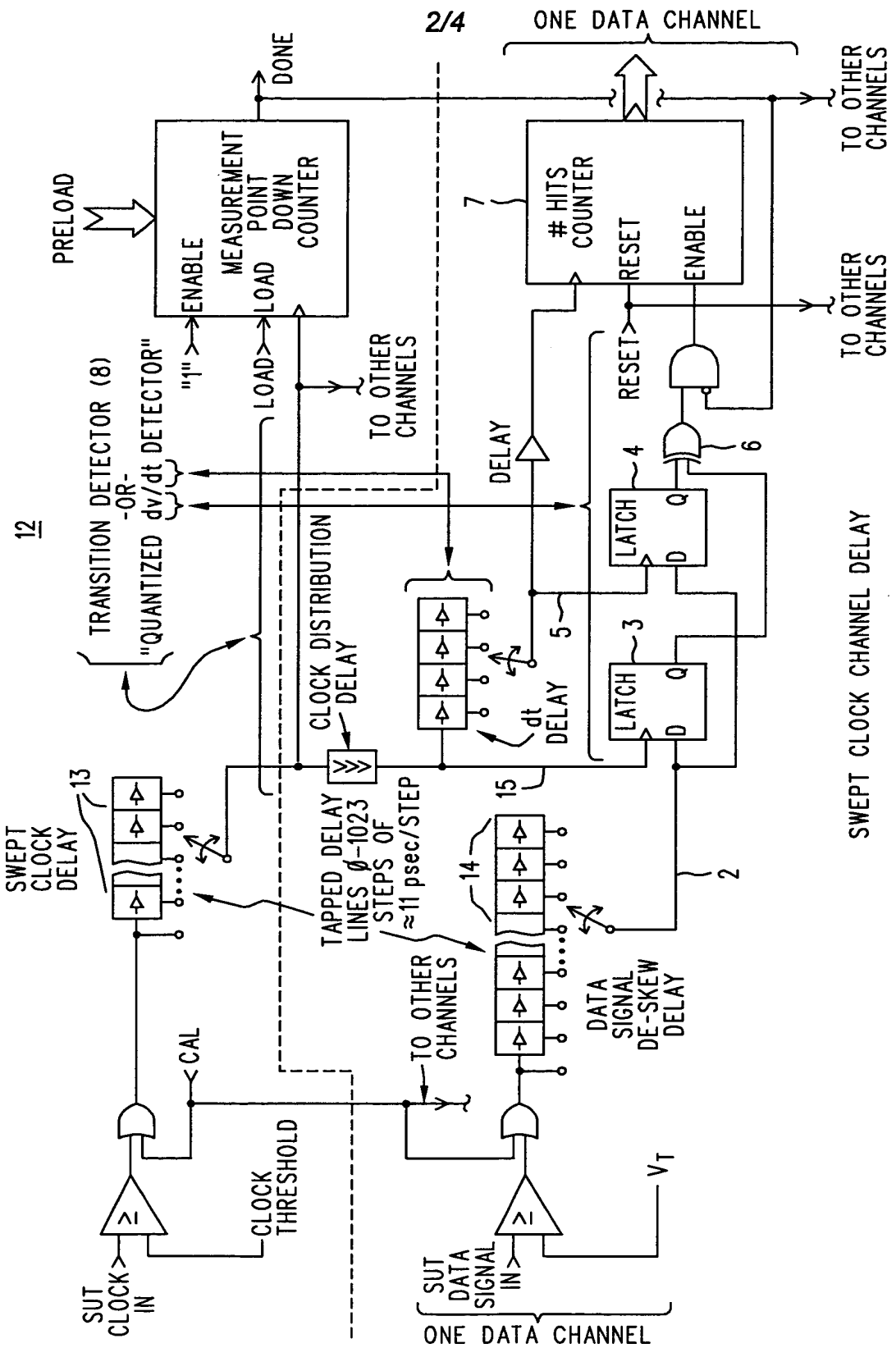


FIG. 1 (PRIOR ART)



SWEPT CLOCK CHANNEL DELAY

FIG. 2 (PRIOR ART)

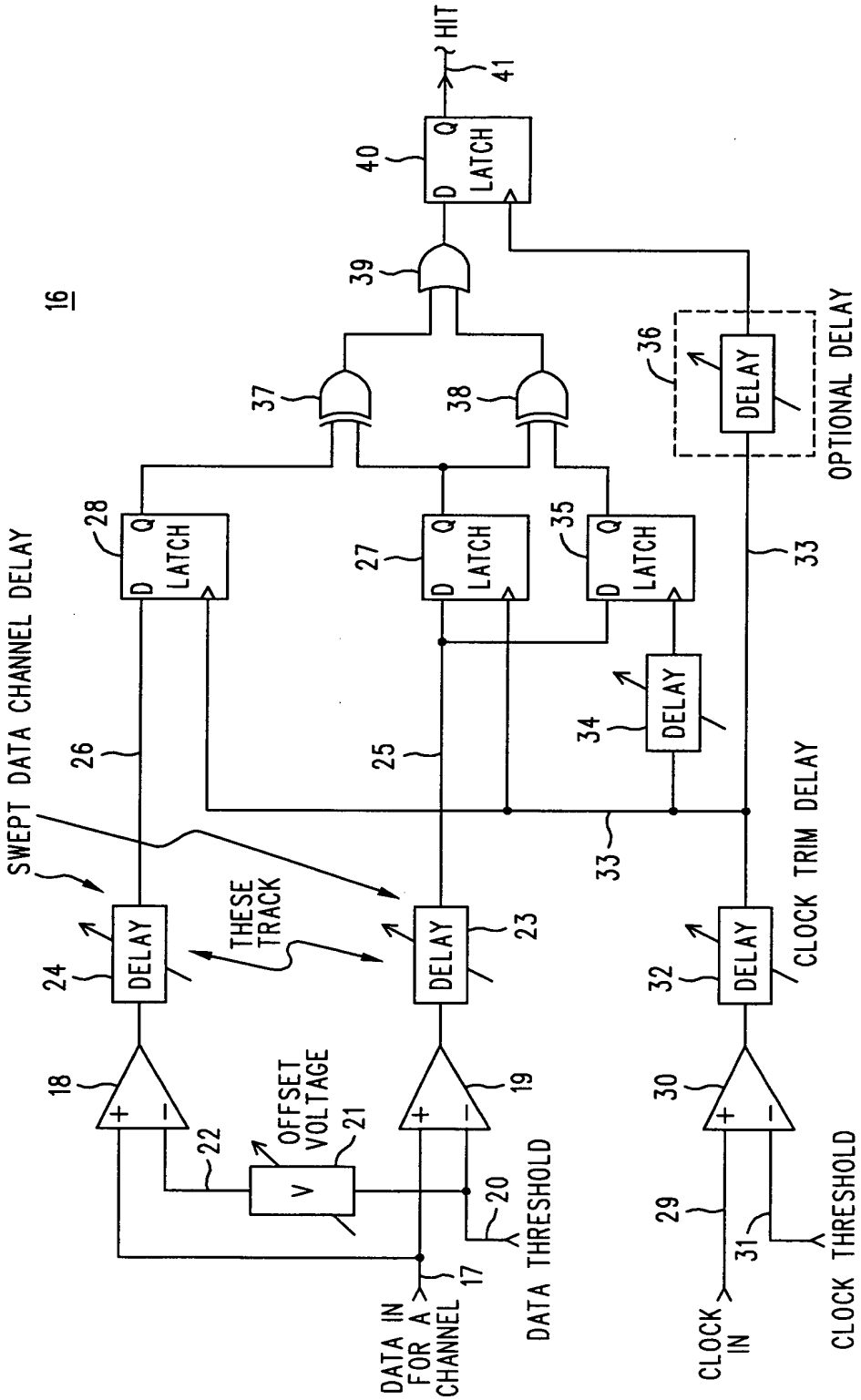

16
HIT
41
40
LATCH
39
37
38
36
DELAY
OPTIONAL DELAY
28
27
35
33
34
26
25
SWEPT DATA CHANNEL DELAY
THESE TRACK
24
23
32
CLOCK TRIM DELAY
18
19
30
OFFSET VOLTAGE
21
22
20
29
31
DATA IN FOR A CHANNEL
17
DATA THRESHOLD
CLOCK IN
CLOCK THRESHOLD

FIG. 3

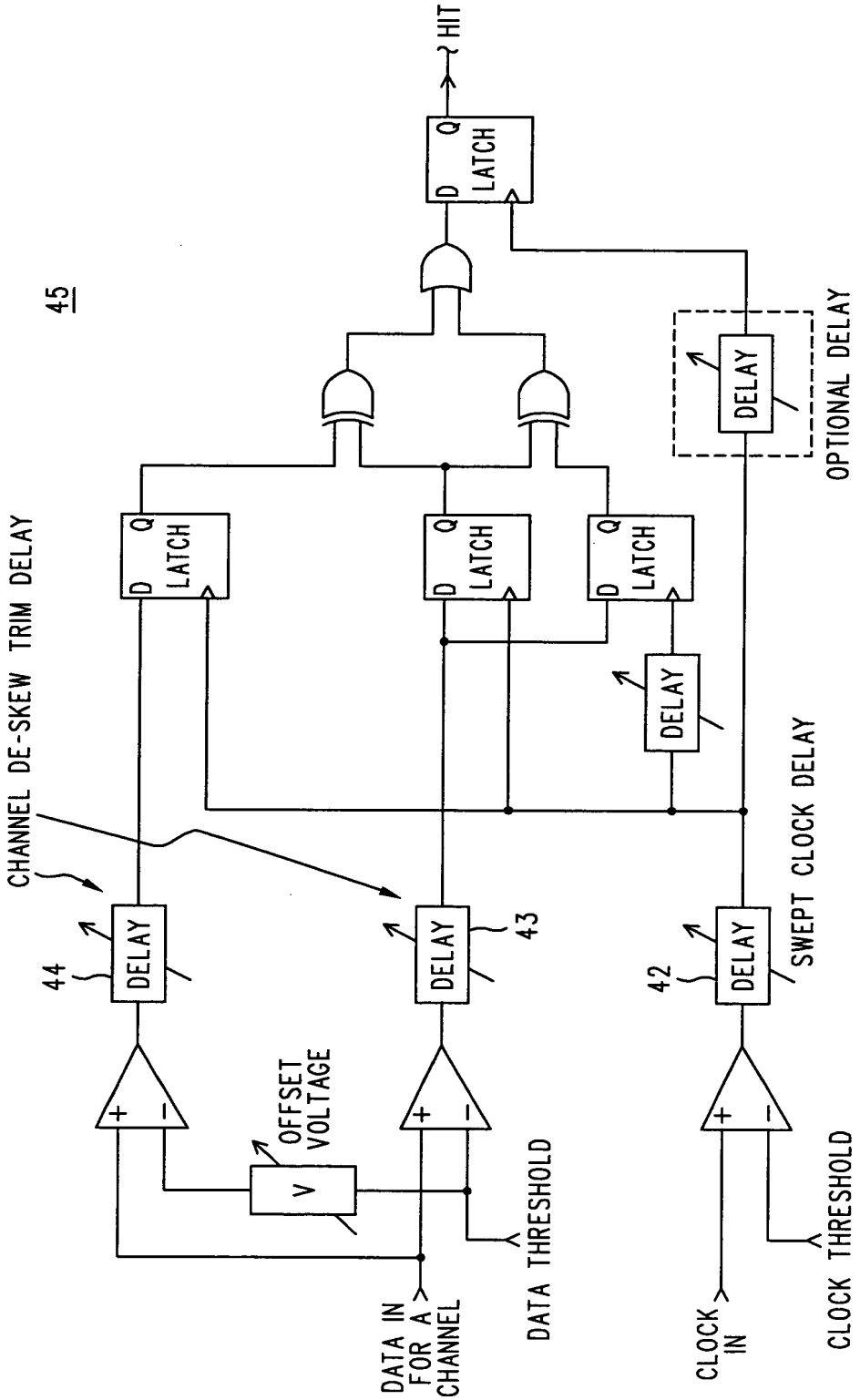

HIT
LATCH
D
Q
45
DELAY
OPTIONAL DELAY
CHANNEL DE-SKEW TRIM DELAY
44
43
42
SWEPT CLOCK DELAY
OFFSET VOLTAGE
V
DATA IN FOR A CHANNEL
DATA THRESHOLD
CLOCK IN
CLOCK THRESHOLD

FIG. 4